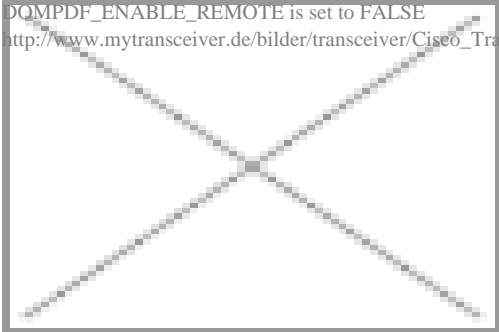


## X2 Transceiver technical info and data sheets

DOMPDF\_ENABLE\_REMOTE is set to FALSE  
[http://www.mytransceiver.de/bilder/transceiver/Cisco\\_Transceiver/X2-10GB-LR-C\\_net.jpg?id=193&time=1409050069](http://www.mytransceiver.de/bilder/transceiver/Cisco_Transceiver/X2-10GB-LR-C_net.jpg?id=193&time=1409050069)

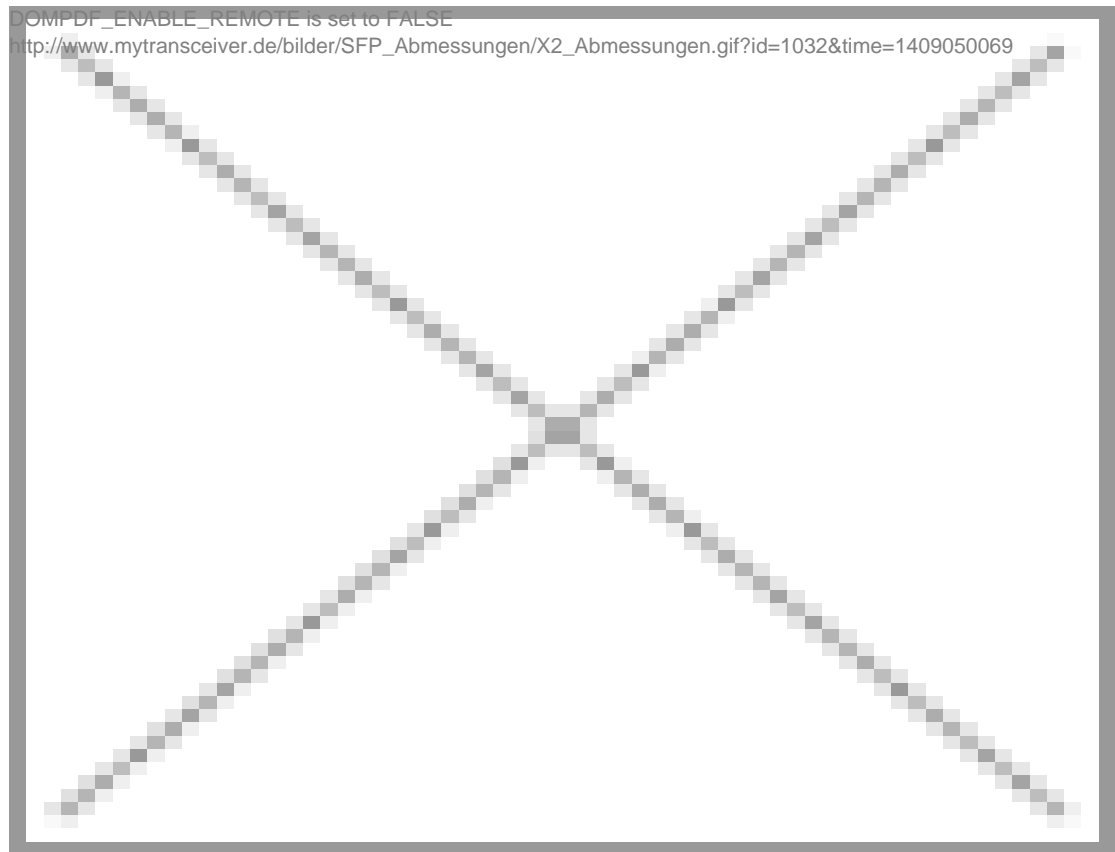


The X2 group was announced on July 22, 2002, and published their specification on February 13, 2003. Issue 3.0 of the XENPAK MSA was transferred to the Small Form Factor committee as document INF-8474 on September 18, 2002.

Its development was based on former XENPAK standards. The X2 10 Gbit/s transceiver's inner function is almost similar with XENPAK that also can use one transceiver to fulfill all 10 Gigabit Ethernet (10GbE) optical port function. X2 transceiver is about half size of XENPAK, which enables it suitable for density installations.

The X2 transceiver is a hot pluggable in the Z-direction module that is usable in typical router line card applications, Storage, IP network and LAN and compliant to XENPAK MSA. The XENPAK transceiver is a fully integrated 10.3 Gbit/s optical transceiver module that consists of a 10.3 Gbit/s optical transmitter and receiver, XAUI interface, MUX and DEMUX with clock and data recovery(CDR).

### **X2 casing (mm)**



## X2 Transceiver Data sheets

| description                               | rate        | reach | wave   |
|---|-------------|-------|--------|
| <a href="#">10 Gigabit X2 Transceiver</a> | 10.3125Gb/s | ?2KM  | 850nm  |
| <a href="#">10 Gigabit X2 Transceiver</a> | 10.3125Gb/s | 80KM  | 1550nm |
| <a href="#">10 Gigabit X2 Transceiver</a> | 10.3125Gb/s | 40KM  | 1550nm |
| <a href="#">10 Gigabit X2 Transceiver</a> | 10.3125Gb/s | 10KM  | 1310nm |